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PATENT, TRADEMARK
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Date: October 27, 2004

FACSIMILE COVER LETTER

Facsimile Number: (703) 872-9306

To: Examiner A. Bello
Group Art Unit 2633, USPTO


From: Mr. Daniel J. Stanger
MATTINGLY, STANGER & MALUR, P.C.

Re: USSN 09/924,589
Attorney Docket No.: ASA-1018

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following listed documents are being facsimile transmitted to the U.S. Patent and Trademark Office on the date shown below:

REQUEST FOR CLARIFICATION.


Daniel J. Stanger
Reg. No. 32,846

October 27, 2004

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ASA-1018

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

T. KATO et al

Serial No. 09/924,589

Group Art Unit: 2633

Filed: August 9, 2001

Examiner: A. BELLO

For: SEMICONDUCTOR INTEGRATED CIRCUIT HAVING RECEIVING AND
TRANSMITTING UNITS FORMED ON A SINGLE SEMICONDUCTOR
CHIP WITH A TEST SIGNAL INPUT PORT

REQUEST FOR CLARIFICATION

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313

October 27, 2004

Sir:

The Applicants have received an Office Action dated September 22, 2004, in which claims 1-7 were rejected under 35 U.S.C. §102(b) as being anticipated by "Baker (U.S. Patent No. 5,586,123)." However, the Form PTO-892 that accompanied the Office Action lists only Hitzeman et al., U.S. 5,856,123. Hitzeman appears to be directed to entirely different subject matter than the present invention.

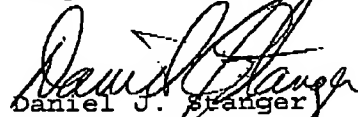
The Applicants believe that a number transposition resulted in the erroneous citation of Hitzeman rather than Baker. However, for clarity, the Applicants request the Examiner to issue a new Office Action with a Reset Statutory

U.S. Serial No. 09/924,589

ASA-1018

Period for Response indicating the reference intended to be applied against the claims. Please also send a new form PTO-Form 892 containing a complete list of the art intended to be of record.

Respectfully submitted,



Daniel J. Stanger
Registration No. 32,846
Attorney for Applicants

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Date: October 27, 2004